

RoHS Compliant

NOTES:

1.MATERIAL:

MOLDING: LCP UL94 V-0

CONTACT: COPPER ALLOY.

GOLD PLATED 30u" Min ON CONTACT AREA, 100u"

Min TIN (LEAD FREE) ON SOLDER AREA,

SHELL: SUS304-H, T=0.30±0.03mm

50u" NICKEL PLATING OVER ALL.

SHILD: SUS304-H, T=0.12±0.03mm

2.MECHANICAL:

INSERTION: 5~20N.

EXTRACTION: 8~20N.

DURABILITY: 10000 CYCLES

3.ELECTRICAL:

CURRENT: 5A MIN FOR VBUS;

0.25A MIN FOR OTHER.

VOLTAGE: 5VAC MAX

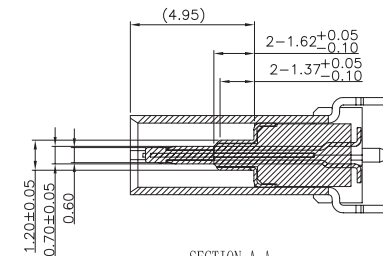
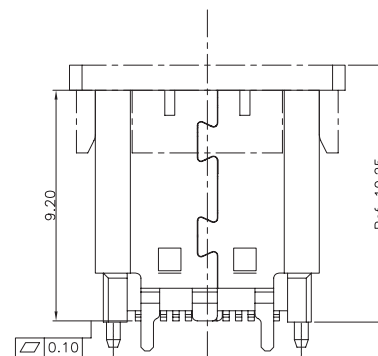
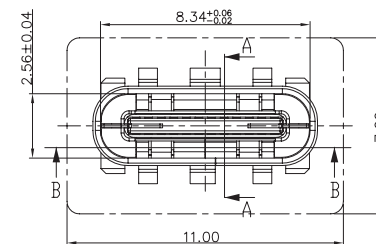
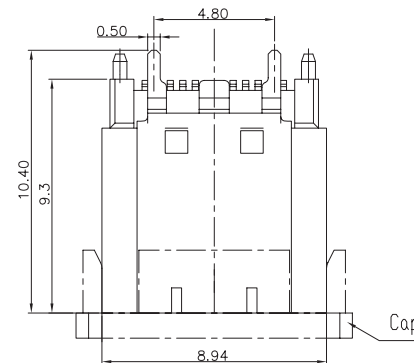
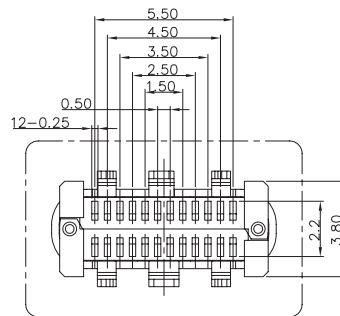
WITHSTANDING VOLTAGE: 100V AC.

CONTACT RESISTANCE: 40mΩ MAX.

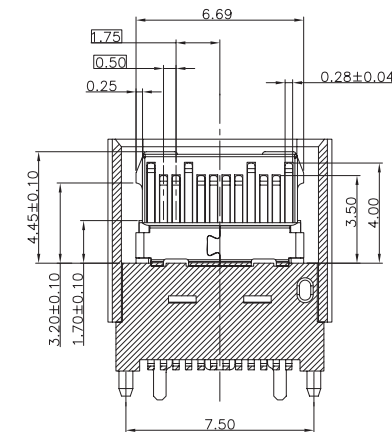
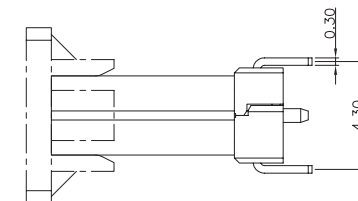
INSULATION RESISTANCE: 100MΩ MIN.

4.ENVIRONMENTAL

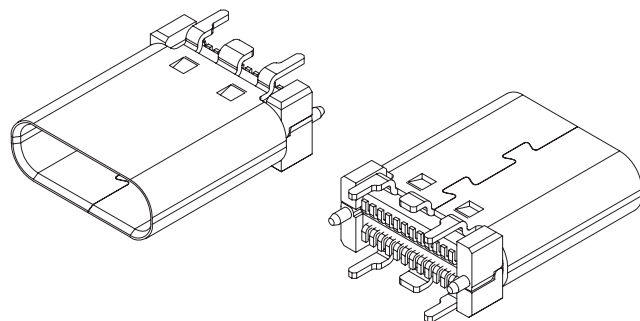
TEMPERATURE RANGE -55°C ~ +85°C



SECTION A-A
SCALE 1:1

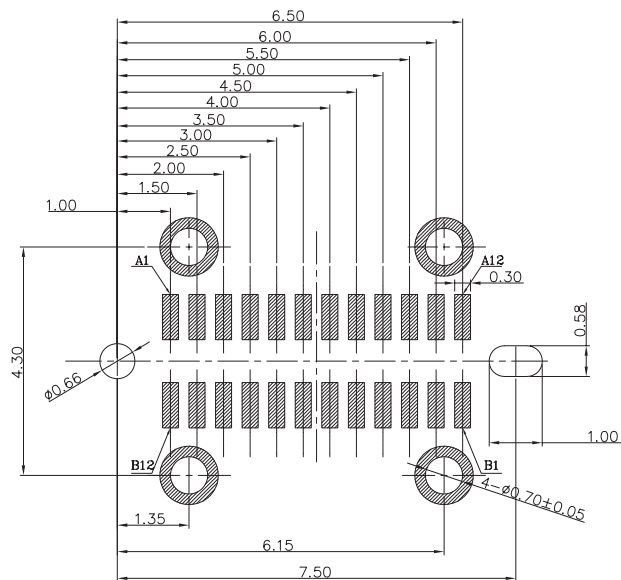


SECTION B-B
SCALE 1:1



6					PART NAME	USB JACK	* LVE * LAKE-VIEW ELECTRONICS CO., LTD.			
5						3.1 C TYPE				
4					PART No.	CU3-V224-B	VIEW		UNIT	mm
3					DESIGNER	YAOFEI WANG	VER	1.2	ANGLES	± 1.5°
2					CHECKED BY	DELIANG LIU	SHEET	1 OF 2	TOLERANCE	30≤L ± 0.30
1					APPROVED BY	David Chen	DATE	'07.06.13		10≤L≤30 ± 0.20
NO.	DESCRIPTION	QTY	MATERIAL	PLATING						L≤10 ± 0.20

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RECOMMENDED P.C.B. LAYOUT (T:1.00mm)
TOLERANCE UNSPECIFIED $\pm 0.05\text{mm}$
SCALE: 2:1

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSTXp1	Positive half of first SuperSpeed TX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSTXn1	Negative half of first SuperSpeed TX differential pair
A4	VBUS	Bus Power	B9	VBUS	Bus Power
A5	CC1	Configuration Channel	B8	CC1	Configuration Channel
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dp1	Positive half of the USB 2.0 differential pair-Position 1
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dn1	Negative half of the USB 2.0 differential pair-Position 1
A8	RFU1	Reserved for Future Use (RFU)	B5	RFU1	Reserved for Future Use (RFU)
A9	VBUS	Bus Power	B4	VBUS	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSRXn2	Negative half of second SuperSpeed RX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSRXp2	Positive half of second SuperSpeed RX differential pair
A12	GND	Ground return	B1	GND	Ground return

6					PART NAME	USB JACK 3.1 C TYPE	LAKE-VIEW ELECTRONICS CO., LTD.			
5					PART No.	CU3-V224-B				
4					DESIGNER	YAOFEI WANG	VIEW		UNIT	mm
3					CHECKED BY	DELIANG LIU	VER	1.2	ANGLES	$\pm 1.5^\circ$
2					APPROVED BY	David Chen	SHEET	2 OF 2	TOLERANCE	$30 \leq L$ ± 0.30
1							DATE	'07.06.13		$10 \leq L \leq 30$ ± 0.20
NO.	DESCRIPTION	QTY	MATERIAL	PLATING						$L \leq 10$ ± 0.20